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Substitute for form 1449B/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known #2

Application Number	To be Assigned
Filing Date	Herewith
First Named Inventor	Roni Rosner, et al.
Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	042390.P12485

UC932 U.S. PTO  
10/03/02

01/02/02

## OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
EBK		L. GWENNAP, "Transmeta Rewrites the Rules, Linux Journal, #72, April, 2000, pp. 126, 130, 131.	
EBK		"IEEE Standard for Binary Floating-Point Arithmetic," ANSI/IEEE Standard 754-1985, Institute of Electrical and Electronics Engineers, August 1985, pp. 1-18.	
EBK		A. PELEG and U. WEISER, "MMX Technology Extension to the Intel Architecture," IEEE Micro, 16(4), pp. 42-50, Aug. 1996	
EBK		S. K. RAMAN, et al., "Implementing Streaming SIMD Extensions on the Pentium III Processor," IEEE Micro, 20(4), pp. 47-57, July/Aug. 2000	
EBK		G.M. SILBERMAN and K. EBCIOGLU, "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures," IEEE Computer, 26(6), pp. 39-56, June 1993	
EBK		R.L. SITES, et al., "Binary Translation," Communications of the ACM, 36(2), 14 pages, Feb. 1993	
EBK		M. UPTON, "The Intel Pentium 4 Processor," in <a href="http://www.intel.com/pentium4">http://www.intel.com/pentium4</a> , Oct. 2000, pp. 1-15.	

Examiner  
Signature

C. B. K.

Date

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